

PATENT

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

Applicants: Kim et al.

Serial No.: 10/032,687

Filed: December 28, 2001

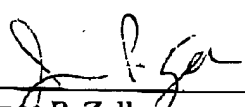
For: METHOD OF FORMING  
WIRING IN SEMICONDUCTOR  
DEVICES

Group Art Unit: 2822

Examiner: Maria F. Guerrero

I hereby certify that this paper is being  
facsimile transmitted to the United States  
Patent and Trademark Office, Fax. No. (703)  
308-7724, on the date shown below:

June 3, 2003

  
James P. Zeller  
Reg. No. 28,491

## RESPONSE TO FINAL REJECTION UNDER 37 C.F.R. §1.116

FAX RECEIVED

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

JUN 3 2003

TECHNOLOGY CENTER 2800

Sir:

In response to the final official action dated April 3, 2003, and pursuant to 37 C.F.R. §1.116, the period for response to which extends through July 3, 2003, entry of the following amendment and remarks is respectfully requested to place the application in condition for allowance or, alternatively, in better form for appeal.

IN THE CLAIMS:

Please amend claim 1, as follows:

- Open*
1. (Previously Amended) A method of forming wiring in a semiconductor device, comprising the steps of :